

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bedichek, et al. Confirmation No.: 7303
Serial No. : 09/417,332 Group Art Unit: 2183
Filed : 10/13/1999 Examiner: Ellis, R.
For : METHOD FOR INTEGRATION OF INTERPRETATION AND
TRANSLATION IN A MICROPROCESSOR

REQUEST FOR CONTINUED EXAMINATION

Assistant Commissioner for Patents & Trademarks
Washington, D.C. 20231

Sir:

Applicants respectfully request reconsideration and continued examination of the present application in light of the concurrently submitted Information Disclosure Statement and Amendments. Immediately after this transmittal page on a separate page is a complete listing of the Claims including Amendments, followed by Applicant Argument (or Remarks)